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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|-------------|----------------------|---------------------|------------------|
| 10/034,458 | 12/28/2001 | Gyo Un Choi | CU-2797 VE | 9340 |
| 26530 | 7590 | 12/01/2004 | EXAMINER | |
| LADAS & PARRY LLP 224 SOUTH MICHIGAN AVENUE SUITE 1200 CHICAGO, IL 60604 | | | DIAZ, JOSE R | |
| | | | ART UNIT | PAPER NUMBER |
| | | | 2815 | |

DATE MAILED: 12/01/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

| | | | |
|------------------------------|-------------------------------|-----------------------------|--|
| Office Action Summary | Application No. 10/034,458 | Applicant(s) CHOI ET AL. | |
| | Examiner José R. Díaz | Art Unit 2815 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 September 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 5 and 9 is/are rejected.
- 7) ☒ Claim(s) 2-4, 6-8 and 10-12 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 January 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Drawings

1. The examiner acknowledges applicant's remarks regarding the drawing corrections. However, it seems that the corrected drawings were misplaced and/or not scanned. The examiner respectfully requests Applicant to file a copy of the corrected figures in response to this Office Action.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1, 5 and 9 are rejected under 35 U.S.C. 102(e) as being anticipated by Kim (US Pat. No. 6,392,719 B2).

Regarding claims 1 and 9, Kim teaches a liquid crystal display for testing defects of wiring in a panel, comprising:

a TFT array unit (3) including a plurality of gate lines (1a and 1b) and data lines (2a-2c) formed in a matrix shape, having TFT transistors (3) at the intersection of each of the gate lines and data lines (see fig. 3);

a data pad unit (D/B, D/G and D/R) commonly connected to the plurality of data lines (2a-2c) for receiving signals for driving the data lines (see fig. 3); and

a wiring unit (6-8) for testing defects of the data line being connected in-line between the data pad unit and the data line for testing disconnection and short anomalies of the data line (see fig. 3 and col. 2, ll. 15-16).

Regarding claim 5, Kim further teaches the liquid crystal display recited in claim 1, a gate pad unit (G/O and G/E) commonly connected to the plurality of gate lines (1a-1b) for receiving signals for driving the gate lines (see fig. 3); a wiring unit (4-5) for testing defects of the gate line connected being between the gate pad unit and the gate line, for testing disconnection and short anomalies of the gate line (see fig. 3 and col. 2, ll. 15-16).

Allowable Subject Matter

4. Claims 2-4, 6-8 and 10-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

5. The following is a statement of reasons for the indication of allowable subject matter: the prior art fails to teach, disclose, or suggest, either alone or in combination, a LCD comprising:

a wiring unit for testing defects of the data lines, first data pad connected to the odd data lines, and second data pad connected to the even data lines;

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a wiring unit for testing defects of the gate lines, first gate pad connected to the odd gate lines, and second gate pad connected to the even gate lines; or

a wiring unit for testing defects of the common voltage lines, first common voltage pad connected to the odd common voltage lines, and second common voltage pad connected to the even common voltage lines.

Response to Arguments

6. Applicant's arguments with respect to claims 1, 5 and 9 have been considered but are moot in view of the new ground of rejection.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Yachi et al. (US Pat. No. 5,767,929) discloses LCD comprising shorting ring (abstract); and Kobayashi (US Pat. No. 5,872,610) discloses a method for inspecting a LCD.

Correspondence


Any inquiry concerning this communication or earlier communications from the examiner should be directed to José R. Díaz whose telephone number is (571) 272-1727. The examiner can normally be reached on Monday through Thursday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JRD
11/29/04


TOM THOMAS
SUPERVISORY PATENT EXAMINER
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